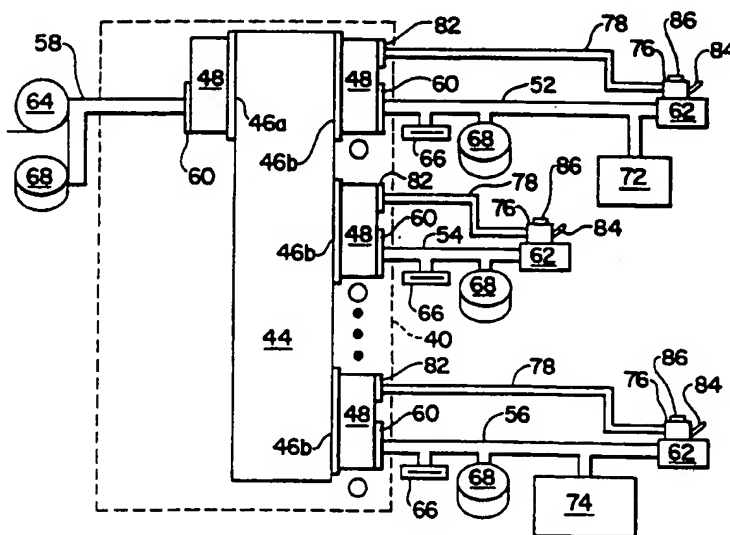




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<p>(21) International Application Number: PCT/US90/01468 (22) International Filing Date: 19 March 1990 (19.03.90) (71)(72) Applicant and Inventor: GAFFORD, Thomas, Austin [US/US]; 311 Barbara Lane, Daly City, CA 94015 (US). (72) Inventor; and (75) Inventor/Applicant (for US only) : ERÖSS, Botond, Gabor [US/US]; 770 Chimalus Ave., Palo Alto, CA 94306 (US). (74) Agents: SCHREIBER, Donald, E. et al.; Niro, Scavone, Haller & Niro, Ltd., 181 West Madison Street, Suite 4600, Chicago, IL 60606-4515 (US).</p>		<p>(81) Designated States: AT (European patent), AU, BE (European patent), CH (European patent), DE (European patent)*, DK, DK (European patent), ES (European patent), FR (European patent), GB (European patent), HU, IT (European patent), JP, KR, LU (European patent), NL (European patent), SE, SE (European patent), SU, US. Published With international search report. With amended claims.</p>

(54) Title: A REPEATER/SWITCH FOR DISTRIBUTED ARBITRATION DIGITAL DATA BUSES



(57) Abstract

The technical field of the invention generally concerns digital computers and, in particular, repeaters or switches (40) for distributed arbitration digital data buses (52, 54, 56 and 58) to which devices (62, 64, 66, 68, 72 and 74) connect in parallel. The bus repeater/switch (40) includes a plurality of bus interface cards (48) that are connected to the distributed arbitration buses (52, 54, 56 and 58) for receiving signals from and transmitting signals to devices (62, 64, 66, 68, 72 and 74) connected thereto. The bus interface cards (48) connect to a control card (44) which allows signals from one of the sharing buses (52, 54 or 56) to be exchanged with the shared bus (58). The bus switch (40) also includes selector switch (84 or 88) for choosing which particular one of the sharing buses (52, 54 or 56) exchanges digital data signals with the shared bus (58). The bus switch (40) responds to signals on the distributed arbitration buses (52, 54, 56 and 58) and to phases of the protocol for those signals so that its presence between pairs of buses (52-58, 54-58 or 56-58) is imperceptible to devices (62, 64, 66, 68, 72 and 74) connected thereto.

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